

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: David A. Beauchaine, et al.
Appl. No.: To Be Assigned
Filed: Concurrently Herewith
For: SUPPORT FIXTURE FOR SEMICONDUCTOR WAFERS
AND ASSOCIATED FABRICATION METHOD

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

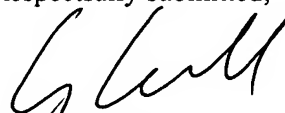
**INFORMATION DISCLOSURE STATEMENT
CITATION UNDER 37 C.F.R. § 1.97**

Sir:

Attached is a list of documents on form PTO-1449. In accordance with the Office waiver published July 11, 2003, copies of the cited U.S. patents and patent application publications are not enclosed. Applicant does enclose copies of any cited foreign patent documents and non-patent literature in accordance with 37 CFR 1.98(a)(2).

It is requested that the Examiner consider these documents and officially make them of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. By submitting the listed documents, Applicant in no way makes any admission as to the prior art status of the listed documents, but is instead submitting the listed documents for the sake of full disclosure.

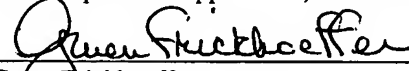
Respectfully submitted,


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Gwen Frickhoeffter

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|---|----------|--|--------------------------------|--|---|---------------------------------------|
| Substitute for form 1449/PTO (Revised 04/2003) INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | | | Complete if Known | | |
| | | | | Application Number | To Be Assigned | |
| | | | | Filing Date | Concurrently Herewith | |
| | | | | First Named Inventor | David A. Beauchaine, et al. | |
| | | | | Group Art Unit | | |
| Examiner Name | | | | | | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 039035/273763 | |
| U. S. PATENT DOCUMENTS | | | | | | |
| Examiner Initials* | Cite No. | Document Number Number - Kind Code (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages of Relevant Figures Appear | |
| | 1 | US-2002/0119641 A1 | 08-29-2002 | Zehavi, et al. | | |
| | 2 | US-2002/0170487 A1 | 11-21-2002 | Zehavi, et al. | | |
| | 3 | US-6,504,233 B1 | 01-07-2003 | Gorczyca, et al. | | |
| | 4 | US-6,617,225 B2 | 09-09-2003 | Boyle, et al. | | |
| OTHER DOCUMENTS | | | | | | |
| Examiner Initials | Cite No. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s) , volume-issue number(s), publisher, city and/or country where published. | | | | English Language Translation Attached |
| | 5 | Asahi Glass Electronic Materials, <i>Roiceram-HS - Ultra High Purity Silicon Carbide</i> , Materials Brochure, undated. | | | | |
| | 6 | SATOSHI KAWAMOTO, FUMITOMO KAWAHARA, TROY COLLARD, Semiconductor Fabtech - 12 th Edition, <i>SiC Applications for Semiconductor Manufacturing</i> , undated. | | | | |
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| Examiner Signature | | | | | Date Considered | |

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

CLT01/4642430v1